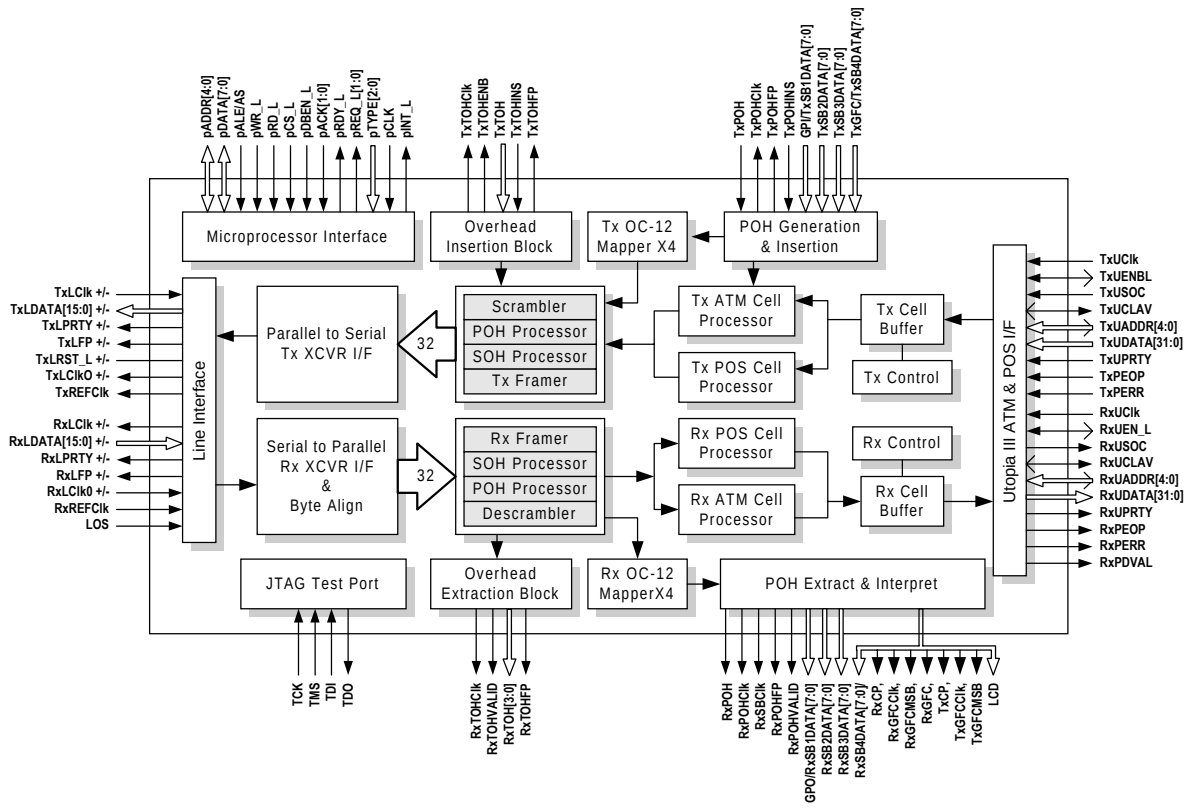
pADDR[4:0]
pDATA[7:0]
pALE/AS
pWR_L
pRD_L
pCS_L
pDBEN_L
pACK[1:0]
pRDY_L
pREQ_L[1:0]
pTYPE[2:0]
pCLK
pINT_L
TxTOHClk
TxTOHENB
TxTOH
TxTOHINS
TxTOHFP
TxPOH
TxPOHClk
TxPOHFP
TxPOHINS
GPI/TxSB1DATA[7:0]
TxSB2DATA[7:0]
TxSB3DATA[7:0]
TxGFC/TxSB4DATA[7:0]
Microprocessor Interface
Overhead Insertion Block
Tx OC-12 Mapper X4
POH Generation & Insertion
TxLClk +/-
TxLDATA[15:0] +/-
TxLPRTY +/-
TxLFP +/-
TxLRST_L +/-
TxLClkO +/-
TxREFClk
RxLClk +/-
RxLDATA[15:0] +/-
RxLPRTY +/-
RxLFP +/-
RxLClk0 +/-
RxREFClk
LOS
Line Interface
Parallel to Serial Tx XCVR I/F
32
Scrambler
POH Processor
SOH Processor
Tx Framer
Tx ATM Cell Processor
Tx POS Cell Processor
Tx Cell Buffer
Tx Control
Serial to Parallel Rx XCVR I/F & Byte Align
32
Rx Framer
SOH Processor
POH Processor
Descrambler
Rx POS Cell Processor
Rx ATM Cell Processor
Rx Control
Rx Cell Buffer
Utopia III ATM & POS I/F
TxUClk
TxUENBL
TxUSOC
TxUCLAV
TxUADDR[4:0]
TxUDATA[31:0]
TxUPRTY
TxPEOP
TxPERR
RxUClk
RxUEN_L
RxUSOC
RxUCLAV
RxUADDR[4:0]
RxUDATA[31:0]
RxUPRTY
RxPEOP
RxPERR
RxPDVAL
JTAG Test Port
Overhead Extraction Block
Rx OC-12 MapperX4
POH Extract & Interpret
TCK
TMS
TDI
TDO
RxTOHClk
RxTOHVALID
RxTOH[3:0]
RxTOHFP
RxPOH
RxPOHClk
RxSBClk
RxPOHFP
RxPOHVALID
GPO/RxSB1DATA[7:0]
RxSB2DATA[7:0]
RxSB3DATA[7:0]
RxSB4DATA[7:0]
RxCP,
RxGFCClk,
RxGFCMSB,
RxGFC,
TxCP,
TxGFCClk,
TxGFCMSB
LCD